

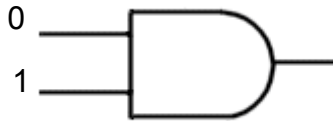
Logic Gate Worksheet

1. A Logic circuit has two inputs being **X** and **Y** fill in all the possible combinations.

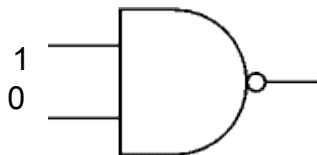
| X | Y |
|----------|----------|
| | |
| | |
| | |
| | |

2. Find the output for the following:

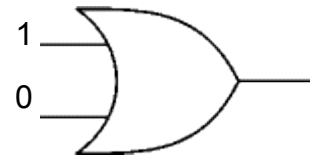
a.



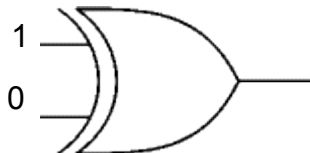
b.



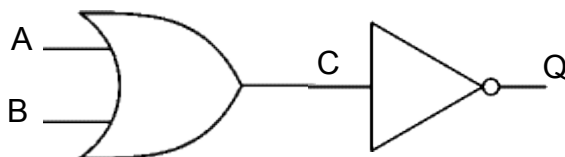
c.



d.



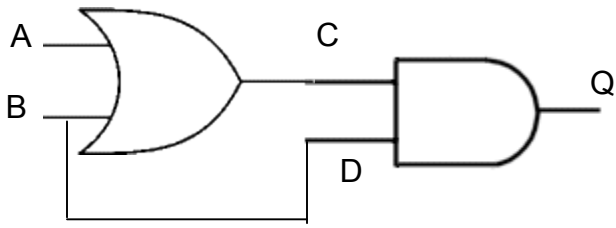
3. The figure below shows a logic circuit and its incomplete truth table. Complete the below truth table.



| A | B | C | Q |
|----------|----------|----------|----------|
| 0 | 0 | | |
| 0 | 1 | | |
| 1 | 0 | | |
| 1 | 1 | | |

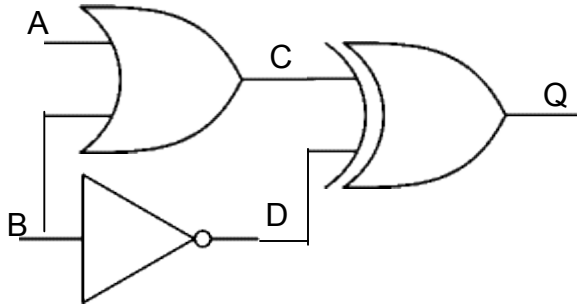
4. The figure below shows a logic circuit and its incomplete truth table. Complete its truth table.

| A | B | C | D | Q |
|----------|----------|----------|----------|----------|
| 0 | 0 | | | |
| 0 | 1 | | | |



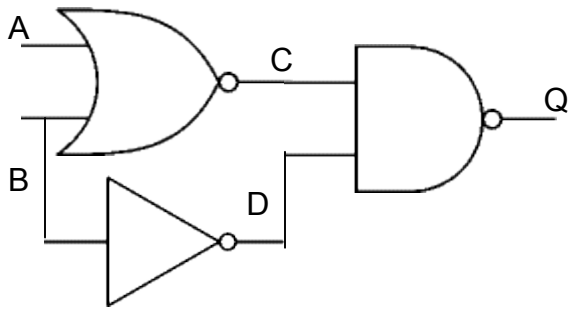
| | | | | |
|---|---|--|--|--|
| 1 | 0 | | | |
| 1 | 1 | | | |

5. The figure below shows a logic circuit and its incomplete truth table. Complete the below truth table.



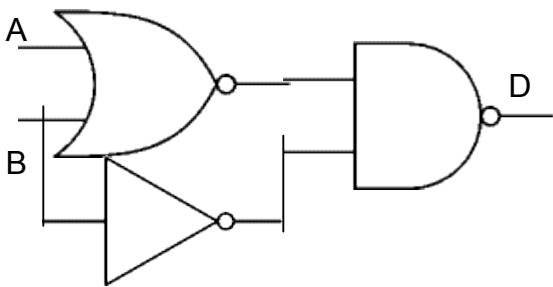
| A | B | C | D | Q |
|---|---|---|---|---|
| 0 | | | | |
| 0 | | | | |
| 1 | | | | |
| 1 | | | | |

6. The figure below shows a logic circuit and its incomplete truth table. Complete the below truth table.



| A | B | C | D | Q |
|---|---|---|---|---|
| | 0 | | | |
| | 1 | | | |
| | 0 | | | |
| | 1 | | | |

7. For the logic circuit below complete the truth table.



| A | B | | | D |
|---|---|--|--|---|
| 0 | 0 | | | |
| 0 | 1 | | | |
| 1 | 0 | | | |
| 1 | 1 | | | |